Advanced Memory and Shape Analyses

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Introduction

2) Relational shape analysis based on separation logic

3 Type-based analysis

4 Comparison and conclusion

Reason 1: Memory is a key program property

Structural invariants on memory are the backbone of the proof in systems programs.

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Structural invariants on memory are the backbone of the proof in systems programs.

"Much of the kernel-call code is directed at maintaining [data-structure] invariants" – Walker et al., Specification and Verification of the UCLA Unix Security Kernel, 1980

"There are four main categories of invariants in our proof: 1. low-level memory invariants, 2. typing invariants, 3. data structure invariants, and 4. algorithmic invariants. [...] 80% of the effort [...] went into establishing invariants."

- Klein et al., Comprehensive Formal Verification of an OS Microkernel, 2015

Reason 2: A key safety and cybersecurity property

Memory safety is key for safety and security of systems software

- Memory corruption is what makes C programming painful (crash, complex debugging, etc.)
- Main cybersecurity attack vector (buffer overflows, use-after-free, etc.)

Reason 2: A key safety and cybersecurity property

Memory safety is key for safety and security of systems software

- Memory corruption is what makes C programming painful (crash, complex debugging, etc.)
- Main cybersecurity attack vector (buffer overflows, use-after-free, etc.)

"70% of the vulnerabilities addressed through a security update each year continue to be memory safety issues." Microsoft

"63% of 2019's exploited 0-day vulnerabilities fall under memory corruption." Google project0

" Future Software Should Be Memory Safe" White House Press Release, Feb. 2024

Reason 3: General purpose analysis of C

Without a good memory abstraction, the analysis is limited to situations where the abstract state is a finite list of known memory cells.

- → In practice:
 - embedded systems programs (no dynamic memory allocation, no recursion), and
 - whole-program analysis (that cannot analyze parts of the program in isolation).

Reason 3: General purpose analysis of C

Without a good memory abstraction, the analysis is limited to situations where the abstract state is a finite list of known memory cells.

- ➔ In practice:
 - embedded systems programs (no dynamic memory allocation, no recursion), and
 - whole-program analysis (that cannot analyze parts of the program in isolation).

Counter examples:

- Analyzing a function which is not main (e.g., a library function).
- Analyzing a program that calls unknown functions pointers, or large/unknown libraries.
- Analyzing a program with an unbounded recursion;
- Analyzing a program that allocates an array with a variable length;
- Analyzing a program that calls malloc in a loop;
- Ubiquitous situations!

```
int i = 100;
int x = 0;
while(i > 1) {
   i--;
}
int x = 42 / i;
```

Abstracts each numeric variable by an interval that over-approximate its possible values.

int i = 100;
int x = 0;
while(i > 1) {
 i--;
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int x = 42 / i;

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int i = 100; • $i \in \{100\}$ int x = 0; • $i \in \{100\}, x \in \{0\}$ while(i > 1) { • $i \in \{100\}, x \in \{0\}$ i--; } int x = 42 / i;



Abstracts each numeric variable by an interval that over-approximate its possible values.

int i = 100; •
$$i \in \{100\}$$

int x = 0; • $i \in \{100\}, x \in \{0\}$
while(i > 1) { • $i \in [99, 100], x \in \{0\}$
i--; • $i \in \{99\}, x \in \{0\}$

int x = 42 / i;



int i = 100; •
$$i \in \{100\}$$

int x = 0; • $i \in \{100\}, x \in \{0\}$
while(i > 1) { • $i \in [98, 100], x \in \{0\}$
 $i--; • i \in [98, 99], x \in \{0\}$
}
int x = 42 / i;

int i = 100; •
$$i \in \{100\}$$

int x = 0; • $i \in \{100\}, x \in \{0\}$
while(i > 1) { • $i \in [98, 100], x \in \{0\}$
i--; • $i \in [97, 99], x \in \{0\}$
}
int x = 42 / i;



int i = 100;
int x = 0;
while(i > 1) {
$$i \in \{100\}, x \in \{0\}$$

i--;
 $i \in [2, 100], x \in \{0\}$
 $i \in [1, 99], x \in \{0\}$
 $i \in \{1\}, x \in \{0\}$
int x = 42 / i;

int i = 100;
int x = 0;
while(i > 1) {
$$i \in \{100\}, x \in \{0\}$$

 $i \in \{100\}, x \in \{0\}$
 $i \in [2, 100], x \in \{0\}$
 $i \in [1, 99], x \in \{0\}$
 $i \in \{1\}, x \in \{0\}$
int x = 42 / i;
 $i \in \{1\}, x \in \{42\}$

- Abstract interpretation automatically infers invariants of the program.
- This can be used to automatically prove program properties.
 - Here: no division by zero (an example of *run-time error*)
 - Other runtime errors: integer overflow
 - Memory-related errors: buffer overflow, null pointer dereference, invalid cast...

Numerical and memory abstractions

Abstract interpretation = automatic computation of abstractions representing program properties. Some numeric abstractions:

Example (Intervals)

// $x \in [-9, 4]$ y := x * x // $x \in [-9, 4] \land y \in [0, 81]$

Example (Linear equations) // y := x * x// $y \ge x \land y \ge -x$

What about memory abstractions? A hard, unsolved problem:

However, while for numerical domains, we have nice open-source libraries that can easily be embedded into larger use-cases, it was noted that this is hardly the case for [data structures] domains

- Dagstuhl seminar on Theoretical Advances and Emerging Applications in Abstract Interpretation

→How to structure and interface with memory abstractions?

Three structures of abstract interpreters



$$p.x := p.x + p.y$$

$$\mathtt{p} \mapsto \{\mathtt{x}{=}[3{-}7], \mathtt{y}{=}[2{-}4]\}$$

Value-based analysis allows clean separation between the numeric and memory abstractions.

Three structures of abstract interpreters

struct	point	{	int	x;	int	y;	}]	p;	
--------	-------	---	-----	----	-----	----	-----	----	--

Non-relational	Variable/Lvalue-based	
$\mathtt{p} \mapsto \{\mathtt{x}{=}[1{-}3], \mathtt{y}{=}[2{-}4]\}$	$\mathtt{p.x} \in [1{-}3] \land \mathtt{p.y} \in [2{-}4]$	
Value, TIS-Analyzer	Eva, Astrée, MOPSA	
 No relations 	+ Relations between vars	
+ Expr-composable	 Not expr-composable 	
$[1{-}3] + [2{-}4] = [3{-}7]$	p.x + p.y = ?	
+ Known structure	+ Known structure	

p.x := p.x + p.y

$$p \mapsto \{x = [3-7], y = [2-4]\}$$
 $p.x \in [3-7] \land p.y \in [2-4]$

Value-based analysis allows clean separation between the numeric and memory abstractions.

Three structures of abstract interpreters

struct point { int x; int y; } p;

Non-relational	Variable/Lvalue-based	Value/SSA-based		
$\mathtt{p} \mapsto \{\mathtt{x}{=}[1{-}3], \mathtt{y}{=}[2{-}4]\}$	$\mathtt{p.x} \in [1{-}3] \land \mathtt{p.y} \in [2{-}4]$	$\begin{array}{c} \mathtt{p} \mapsto \{\mathtt{x}{=}\alpha, \mathtt{y}{=}\beta\} \land \\ \alpha \in [1{-}3] \land \beta \in [2{-}4] \end{array}$		
Value, TIS-Analyzer	Eva, Astrée, MOPSA	Codex, RMA, MemCAD		
 No relations 	+ Relations between vars	+ Relations between $lpha$		
+ Expr-composable	 Not expr-composable 	+ Expr-composable		
$[1{-}3] + [2{-}4] = [3{-}7]$	p.x + p.y = ?	$\alpha + \beta = (\alpha + \beta)$		
+ Known structure	+ Known structure	 Need study 		

p.x := p.x + p.y

$$\mathbf{p} \mapsto \{\mathbf{x} = [3-7], \mathbf{y} = [2-4]\} \qquad \mathbf{p} \cdot \mathbf{x} \in [3-7] \land \mathbf{p} \cdot \mathbf{y} \in [2-4] \qquad \begin{array}{c} \mathbf{p} \mapsto \{\mathbf{x} = \alpha + \beta, \mathbf{y} = \beta\} \land \\ (\alpha + \beta) \in [3-7] \land \beta \in [2-4] \\ \land \alpha \in [1-3] \end{array}$$

Value-based analysis allows clean separation between the numeric and memory abstractions.

Variable vs value-based analysis for memory

Example (Microsoft CheckedC)

```
int n;
int *ptr : count(n);
if(...)
 n++; // ptr: count(n-1)
else if(...)
 n = n * n; //ptr: count(sqrt(n))
else if(...)
 n = n / 2; // ptr: count(2*n) ||
             // ptr:(count(2*n+1))
else if(...)
 ptr++ // ptr: count(n-1);
}
```

- Complicated relation
 - ➔ forbid changing ptr or n

Example (Codex)

```
(int with self = \alpha) n;
int[\alpha] * ptr;
if(...)
  n++; // ptr:int[\alpha]*, n = \alpha + 1
else if(...)
  n = n * n; // ptr:int[\alpha] *
               // n = \alpha * \alpha
else if(...)
  n = n / 2; // ptr:int[\alpha] *
              // n = \alpha / 2
else if(...)
  ptr++ // ptr:int[\alpha]* + 1,n = \alpha
```

Simple relation

Memory abstraction not involved

Problem: renaming values when joining execution paths

Example

```
 \begin{array}{l} // \ p \mapsto \{\mathbf{x} = \alpha, \mathbf{y} = \beta\} \\ \texttt{if}(\ldots) \ \{ \\ \texttt{p.x} := \texttt{p.x} + \texttt{1}; \\ // \ p \mapsto \{\mathbf{x} = \alpha + 1, \mathbf{y} = \beta\} \\ \} \\ \texttt{else} \ \{ \\ \texttt{p.x} := \texttt{p.x}; \\ // \ p \mapsto \{\mathbf{x} = \alpha, \mathbf{y} = \beta\} \\ \} \\ // \ p \mapsto \{\mathbf{x} = \phi(\alpha, \alpha + 1), \mathbf{y} = \beta\} \end{array}
```

• What does ϕ mean? Is this related to the ϕ of SSA? (Was unclear for several years...)

- Recent advances: SSA Translation Is an Abstract Interpretation [POPL 2023], Compiling with Abstract Interpretation [PLDI 2024]
- ➔ Allows future integration of Codex memory domains in Eva

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④ Comparison and conclusion

- State analyses: Computes a set of reachable states to verify state properties:
 - Can this program perform a null pointer dereference?
 - Does this program preserve structural invariants of data structures?

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 - Can this program perform a null pointer dereference?
 - Does this program preserve structural invariants of data structures?
- Transformation analyses: Compute abstract transformations, i.e. relations between program input state and output state:
 - Does this program modify the linked list received as an argument?
 - Is this sorting algorithm in-place?

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The RMA (Relational Memory Analysis) plugin

• Abstract transformations as procedure summaries

- State analyses: Computes a set of reachable states to verify state properties:
 - Can this program perform a null pointer dereference?
 - Does this program preserve structural invariants of data structures?
- Transformation analyses: Compute abstract transformations, i.e. relations between program input state and output state:
 - Does this program modify the linked list received as an argument?
 - Is this sorting algorithm in-place?

The RMA (Relational Memory Analysis) plugin

- Abstract transformations as procedure summaries
- Applied to shape analysis using separation logic.

Overview

```
double_append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
```

```
append(k_0,k_1);
```

 $append(k_0,k_2);$

```
}
append(list* l<sub>0</sub>,list* l<sub>1</sub>){
```

```
while (l_0 \rightarrow n \neq 0x0) {l_0 = l_0 \rightarrow n; }
l_0 \rightarrow n = l_1;
```

```
double_append(list* k_0,list* k_1,list* k_2){
h_0^{\sharp}
append(k_0,k_1);
```

 $append(k_0,k_2);$

```
}
append(list* l_0,list* l_1){
```

```
while (l_0 \rightarrow n \neq 0x0) {l_0 = l_0 \rightarrow n; }
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double_append(list* k_0,list* k_1,list* k_2){
h_0^{\sharp}
append(k_0,k_1);
```

```
append(k_0,k_2);
```

```
}
append(list* l<sub>0</sub>,list* l<sub>1</sub>){
h_1^{\sharp}
while(l<sub>0</sub>\rightarrown\neq0x0){l<sub>0</sub>=l<sub>0</sub>\rightarrown;}
l<sub>0</sub>\rightarrown = l<sub>1</sub>;
```

```
double_append(list* k_0,list* k_1,list* k_2){
h_0^{\sharp}
append(k_0, k_1);
```

 $append(k_0,k_2);$

```
}
append(list* l<sub>0</sub>,list* l<sub>1</sub>){
h_1^{\sharp}
while(l<sub>0</sub>\rightarrown\neq0x0){l<sub>0</sub>=l<sub>0</sub>\rightarrown;}
l<sub>0</sub>\rightarrown = l<sub>1</sub>;
h_{19}^{\sharp}
```

```
double append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
                             h_0^{\sharp}
  append(k_0, k_1);
                             h_{20}^{\sharp}
  append(k_0, k_2);
}
append(list* l_0,list* l_1){
  while (l_0 \rightarrow n \neq 0x0) {l_0 = l_0 \rightarrow n; }
```

 $l_0 \rightarrow n = l_1$:

```
double append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
                               h_0^{\sharp}
   append(k_0, k_1);
                               h_{20}^{\sharp}
  append(k_0, k_2);
}
append(list* l_0,list* l_1){
                                                h_{51}^{\sharp}
  while (l_0 \rightarrow n \neq 0x0) \{l_0 = l_0 \rightarrow n;\}
   l_0 \rightarrow n = l_1:
```

```
double append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
                               h_0^{\sharp}
   append(k_0, k_1);
                               h_{20}^{\sharp}
  append(k_0, k_2);
}
append(list* l_0,list* l_1){
                                               h_{51}^{\sharp}
   while (l_0 \rightarrow n \neq 0x0) \{l_0 = l_0 \rightarrow n;\}
```

 h_{39}^{\sharp}

 $l_0 \rightarrow n = l_1$:

```
double append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
                               h_0^{\sharp}
   append(k_0, k_1);
                               h_{20}^{\sharp}
  append(k_0, k_2);
                               h_{40}^{\sharp}
}
append(list* l_0,list* l_1){
   while (l_0 \rightarrow n \neq 0x0) {l_0 = l_0 \rightarrow n; }
   l_0 \rightarrow n = l_1:
```

```
double_append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
```

 $append(k_0,k_1);$

 $append(k_0,k_2);$

while $(l_0 \rightarrow n \neq 0x)$

 $l_0 \rightarrow n = l_1$:

}

append(list* l_0,list* l_1){

- + Precise analysis of procedures
- Analysis of append is repeated for each calling context
- Cannot handle recursive procedures

```
double_append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
```

```
append(k_0,k_1);
```

 $append(k_0,k_2);$

```
}
append(list* l<sub>0</sub>,list* l<sub>1</sub>){
```

```
while (l_0 \rightarrow n \neq 0x0) {l_0 = l_0 \rightarrow n; }
l_0 \rightarrow n = l_1;
```

```
double_append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
```

 $append(k_0,k_1);$

 $append(k_0,k_2);$

}

```
append(list* l<sub>0</sub>,list* l<sub>1</sub>){
```

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 $\{l_0 = l_0 \rightarrow n;\}$ t^{\sharp}

```
double_append(list* k_0,list* k_1,list* k_2){

h_0^{\sharp}

append(k_0, k_1);

append(k_0, k_2);
```

```
append(list* l<sub>0</sub>,list* l<sub>1</sub>){
```

}

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 { $l_0 = l_0 \rightarrow n$; }
 $l_0 \rightarrow n = l_1$;

```
double append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
                                h_0^{\sharp}
   append(k_0, k_1);
                               h_1^{\sharp} = t^{\sharp}(h_0^{\sharp})
   append(k_0, k_2);
}
append(list* l_0,list* l_1){
  while (l_0 \rightarrow n \neq 0 \ge 0) {l_0 = l_0 \rightarrow n; } t^{\sharp}
```

```
double append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
                                  h_0^{\sharp}
   append(k_0, k_1);
                                  h_1^{\sharp} = t^{\sharp}(h_0^{\sharp})
   append(k_0, k_2);
                                  h_2^{\sharp} = t^{\sharp}(h_1^{\sharp})
}
append(list* l_0,list* l_1){
  while (l_0 \rightarrow n \neq 0 \ge 0) {l_0 = l_0 \rightarrow n; } |_{t^{\sharp}}
```

```
double_append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
```

```
append(k_0,k_1);
```

 $append(k_0,k_2);$

}

```
append(list* l<sub>0</sub>,list* l<sub>1</sub>){
```

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 { $l_0 = l_0 \rightarrow n$; }

• Applying an abstract transformation can speed up a state analysis.

```
double_append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
```

```
append(k_0,k_1);
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 $append(k_0,k_2);$

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}
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```

```
while (l_0 \rightarrow n \neq 0x0) {l_0 = l_0 \rightarrow n; }
l_0 \rightarrow n = l_1;
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double_append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
```

```
append(k_0,k_1);
```

 $append(k_0,k_2);$

}

```
append(list* l<sub>0</sub>,list* l<sub>1</sub>){
```

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 $\{l_0 = l_0 \rightarrow n;\}$ t^{\sharp}

```
double_append(list* k_0,list* k_1,list* k_2){
 \downarrow Id(h_0^{\sharp})
 append(k_0,k_1);
```

append(k_0, k_2);

}

```
append(list* l<sub>0</sub>,list* l<sub>1</sub>){
```

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 { $l_0 = l_0 \rightarrow n$; } t^{\sharp}

double_append(list* k₀,list* k₁,list* k₂){

append(
$$\mathbf{k}_0$$
, \mathbf{k}_1); $\int_{t^{\sharp} \circ \mathbf{Id}(h_0^{\sharp})} \mathbf{Id}(h_0^{\sharp})$
append(\mathbf{k}_0 , \mathbf{k}_2);
}
append(list* \mathbf{l}_0 , list* \mathbf{l}_1){
while($\mathbf{l}_0 \rightarrow \mathbf{n} \neq \mathbf{0} \times \mathbf{0}$){ $\mathbf{l}_0 = \mathbf{l}_0 \rightarrow \mathbf{n}$;
 $\mathbf{l}_0 \rightarrow \mathbf{n} = \mathbf{l}_1$;

t♯

double_append(list* k₀,list* k₁,list* k₂){

append(
$$\mathbf{k}_0$$
, \mathbf{k}_1);
append(\mathbf{k}_0 , \mathbf{k}_2);
 $t^{\sharp} \circ \mathrm{Id}(h_0^{\sharp})$
 $t^{\sharp} \circ \mathrm{Id}(h_0^{\sharp})$
 $t^{\sharp} \circ \mathrm{Id}(h_0^{\sharp})$

append(list* l₀,list* l₁){

}

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 { $l_0 = l_0 \rightarrow n$; }
 $l_0 \rightarrow n = l_1$;

```
double_append(list* k<sub>0</sub>,list* k<sub>1</sub>,list* k<sub>2</sub>){
```

```
append(k_0,k_1);
```

 $append(k_0,k_2);$

}

append(list* l₀,list* l₁){

while $(l_0 \rightarrow n \neq 0x)$ $l_0 \rightarrow n = l_1;$

- Composition of relations can produce a new summary from summaries of callee functions.
- Summary was created for a given input state (context)



t[♯]

 $append(k_0,k_1);$

 $append(k_0,k_2);$

}

append(list* l₀,list* l₁){

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 { $l_0 = l_0 \rightarrow n$; }
 $l_0 \rightarrow n = l_1$;

$$\begin{array}{c} \beta_{0,l_0} \\ \hline (seg(\beta_1)) \\ Id \end{array} * T \begin{array}{c} 0x0 \\ \beta_1 \\ \beta_1 \\ \beta_1 \\ \beta_1 \\ \beta_2 \end{array} * T \begin{array}{c} \beta_{2,l_1} \\ list \\ Id \end{array}$$



t[♯]

 $append(k_0,k_2);$

}

append(list* l₀,list* l₁){

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 { $l_0 = l_0 \rightarrow n$; }
 $l_0 \rightarrow n = l_1$;

$$\begin{array}{c} \beta_{0,l_0} \\ \hline (lseg(\beta_1)) \\ Id \end{array} * T \begin{array}{c} 0x0 \\ \beta_1 \\ \beta_1 \\ \beta_1 \\ \beta_1 \\ \beta_2 \end{array} * T \begin{array}{c} \beta_{2,l_1} \\ list \\ Id \end{array}$$



t♯

append(k_0, k_2);

}

append(list* l₀,list* l₁){

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 { $l_0 = l_0 \rightarrow n$; }
 $l_0 \rightarrow n = l_1;$

$$\begin{array}{c} \beta_{0},l_{0} \\ (\textbf{lseg}(\beta_{1})) \\ \textbf{Id} \end{array} *_{T} \begin{array}{c} 0x0 \\ \beta_{1} \\ \beta_{1} \\ \beta_{1} \\ \beta_{2} \end{array} *_{T} \begin{array}{c} \beta_{2},l_{1} \\ \textbf{list} \\ \textbf{Id} \end{array}$$



}

append(list* l₀,list* l₁){

while
$$(l_0 \rightarrow n \neq 0 \ge 0)$$
 $\{l_0 = l_0 \rightarrow n;\}$
 $l_0 \rightarrow n = l_1;$
 t^{\sharp}
Id

Id

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3 Type-based analysis

4 Comparison and conclusion

Separation logic vs type-based analysis

Shape analysis based on separation logic

- Allows very precise invariant
- Only need to provide initial state and inductive predicates (list...)
- Sometimes matching may fail...
- Need to reason about whether lists are separated or cyclic

Type-based analysis

- Weaker invariant
- But need to know less about the memory invariants
- Type-based analysis: lightweight formal method
 - · Goal: prove absence of undefined behaviour, including memory corruption
 - Per-function analysis that can scale to large program
 - Without rewriting in Rust or annotating the function body

Record & array types

Types represent a memory layout. Record types $\tau_1 \times \tau_2$ and array types $\tau[e]$ concatenate types.





Refinement types

Types also represent values. Values in a **refinement type** τ with p fulfill predicate p.





Non-null pointer types

Pointer types $\eta \star$ denote **non null** addresses. Actually, $\eta \star$ is $\eta \star \cup (byte[\mathcal{W}] \text{ with self} = 0)$





Existential types

Existential types: $\exists \alpha : \tau_1 . \tau_2$ bind in τ_2 a symbolic variable α of type τ_1 .

```
def int := byte[4]
def char := byte

def message :=
∃ len:byte[4] with self >= 0.
   message* ×
   char[len]*
```

```
def message_box :=
   byte[4] with self >= 0 ×
   message*
```

```
1 // corresponding C type
2 //
3
4 struct message {
5 struct message *next;
6 char *buffer };
7
8 struct message_box {
9 int length;
10 struct message *first };
```



Lemerre, Rival, Illous, Nicole, Simonnet, Sighireanu

Advanced Memory and Shape Analyses

Parameterized types

Parameterized types $n(e_1, ..., e_\ell)$ use symbolic variables as parameters.

```
def int := byte[4]
def char := byte

def message(len:int) :=
    message(len)★ ×
    char[len]★

def message_box :=
∃ mlen:byte[4] with self >= 0.
    byte[4] with self = mlen ×
    message(mlen)★
```

1 // corresponding C type 2 // 3 4 struct message { 5 struct message *next; 6 char *buffer }; 7 8 struct message_box { 9 int length; 10 struct message *first };



Union types

Union types $\tau_1 \cup \tau_2$ specify that a value belongs to one type τ_i or both.



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Nominal type system





Introduction

2 Relational shape analysis based on separation logic

3 Type-based analysis

4 Comparison and conclusion

Conclusion

- Memory analysis : a fundamental problem that limits applicability of static analysers
- Was an exploratory solution now with a strong theory and fast becoming mature
 - Codex: a library of composable memory abstractions
 - Plan on integrating more with Eva analysis
- Important applications:
 - Modular analysis (integrate abstract interpretation during the development phase, analysis of libraries...)
 - Automated analysis of programs with complex memory invariants
 - Automated verification of memory safety
- Different memory abstractions
 - Separation logic: for precise invariant on data structures
 - Types: a simpler, scalable analysis that handles low-level code
 - Future work: integrate separation logic and type-based analysis